

H013 Online Test

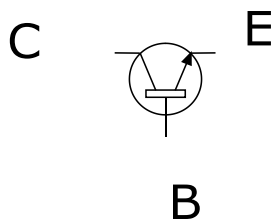
Ref442

Which formula is wrong

A	$db_{AV} = 20 \log V_{out}/V_{in}$	B	$db_{AI} = 20 \log I_{out}/I_{in}$
C	$db_{AP} = 10 \log P_{out}/P_{in}$	D	$A_v = V_{in} / V_{out}$
Answer			

Ref443

The following transistor is



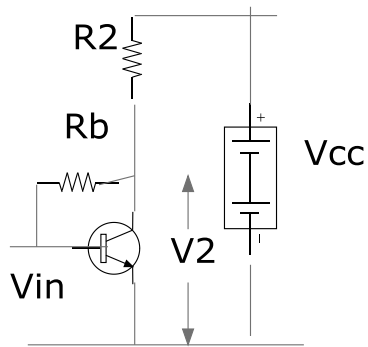
A	NPN Transistor	B	PNP Transistor
C	FET	D	Zener
Answer			

Ref444

The transistor which produces an output for half of input cycle (180°) is

A	Class A	B	Class B
C	Class AB	D	Class C
Answer			

Ref445



This circuit is

A	Base bias	B	Collector bias
C	Voltage divider	D	Dual supply bias
Answer			

Ref446

To achieve high current power gain, voltage gain less than 1, medium to high input resistance (10Ω to 100K Ω) Low output resistance (Tens of ohms), Buffer and isolation stages)

A	Common collector circuit is to be used	B	Common emitter circuit is to be used
C	Common base circuit is to be used	D	
Answer			

Ref447

The following formula

$$A_v = A_i, A_i = I_c / I_E, R_{IN} = r_e, R_o = R_c$$

can be used for

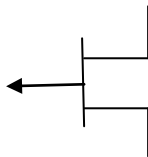
A	CE Amplifier	B	CE Amplifier
C	CB Amplifier	D	
Answer			

Ref448

In _____ transistor, input is given at source and gain is less than 1

A	Common source FET	B	Common drain FET
C	Common gate FET	D	
Answer			

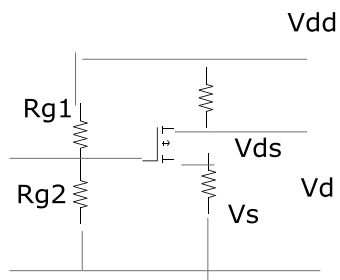
Ref449



This FET is

A	N channel FET	B	P Channel FET
C		D	
Answer			

Ref450



This circuit is

A	JFET bias	B	Voltage divider bias
C	Self bias	D	
Answer			

